

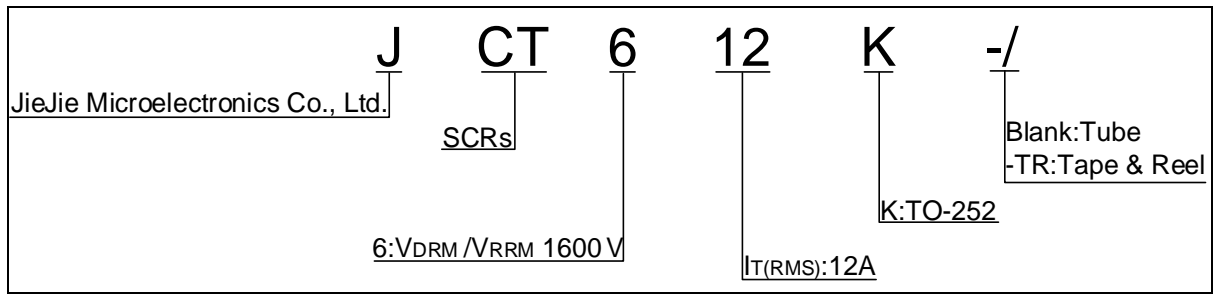


	W
	W
5	kV

)

	Unit
MAX.	
15	mA
1	V
-	V
60	mA
50	mA
-	V s
-	s
-	

ORDERING INFORMATION



MARKING

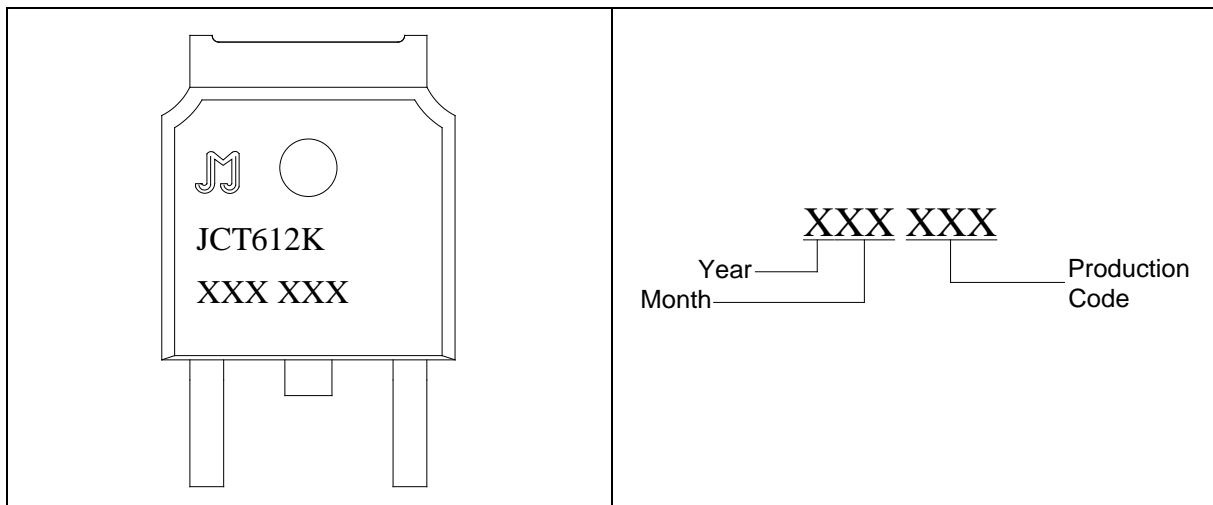


FIG.1: Maximum power dissipation versus RMS on-state current

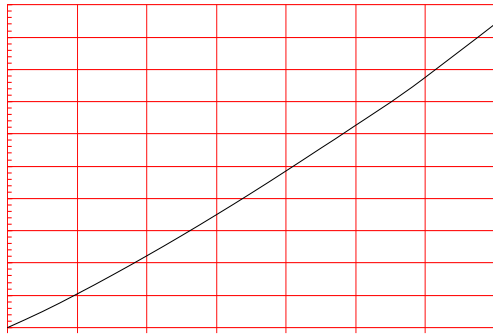


FIG.2: RMS on-state current versus case temperature

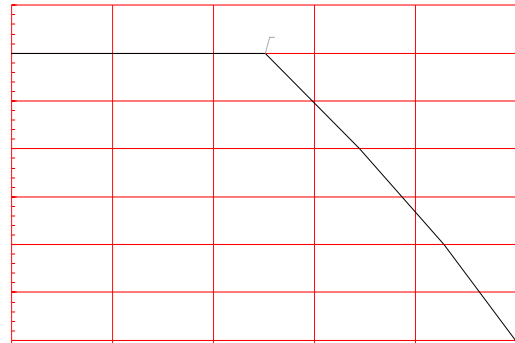
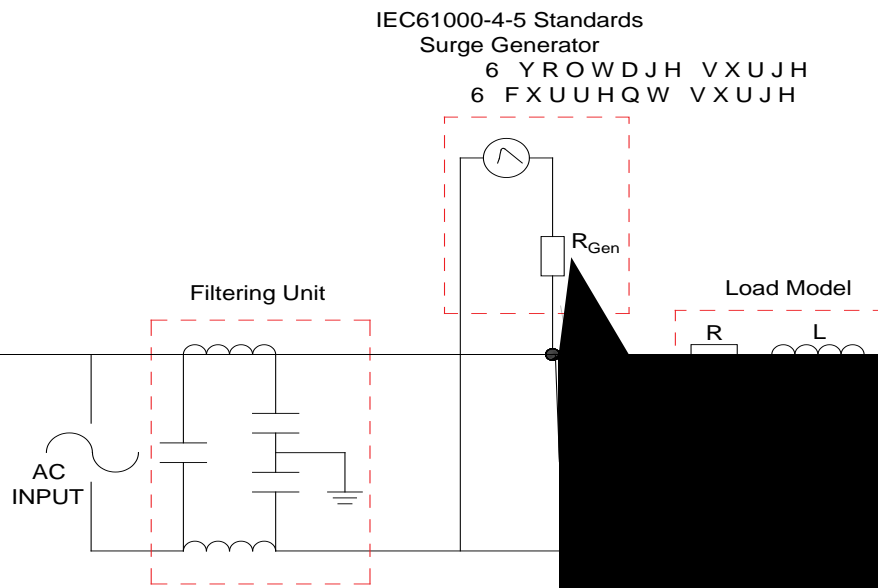


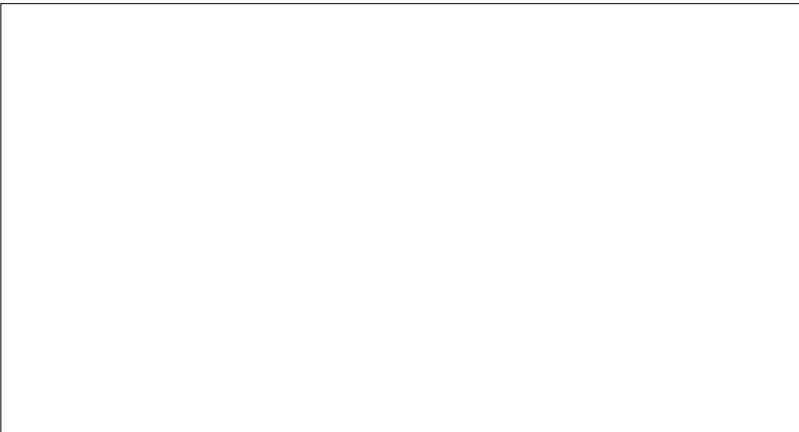
FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper  
WKLFNQHV V P IXOO F\FOH

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.









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