



T1610H-8F 16A TRIAC

Rev.A.1.1

DESCRIPTION:

The T1610H-8F triac i

166

i RD



T1610H-8F

Peak gate current ($t_p=20\mu s$, $T_j=150$)	I_{GM}	4	A
Average gate power dissipation ($T_j=150$)	$P_{G(AV)}$	PT	—

ORDERING INFORMATION

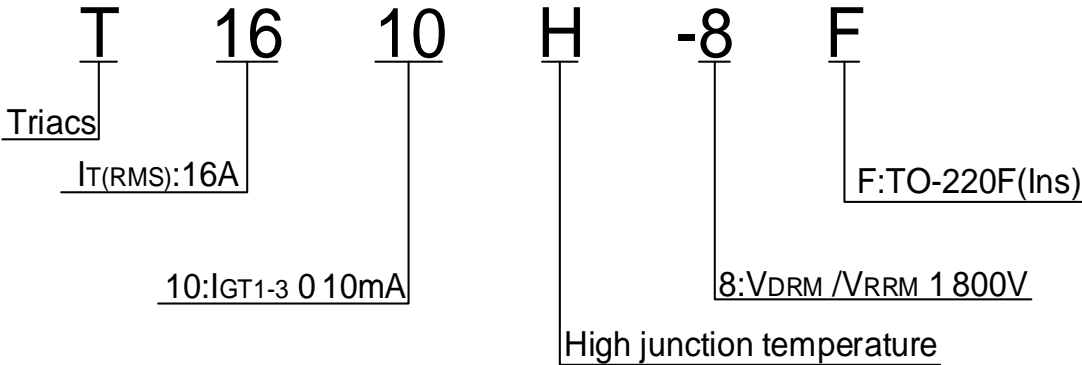


FIG.1: Maximum power dissipation versus RMS on-state current

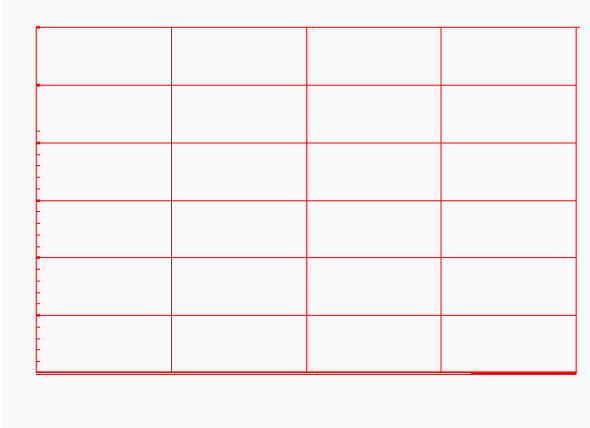


FIG.2: RMS on-state current versus case temperature

FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



T1610H-8F

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond