

JIEJIE MICROELECTRONICS CO., L

Avere gate pow (6 (er))13 dissipatjen150) P_{G(AV)} 1 W

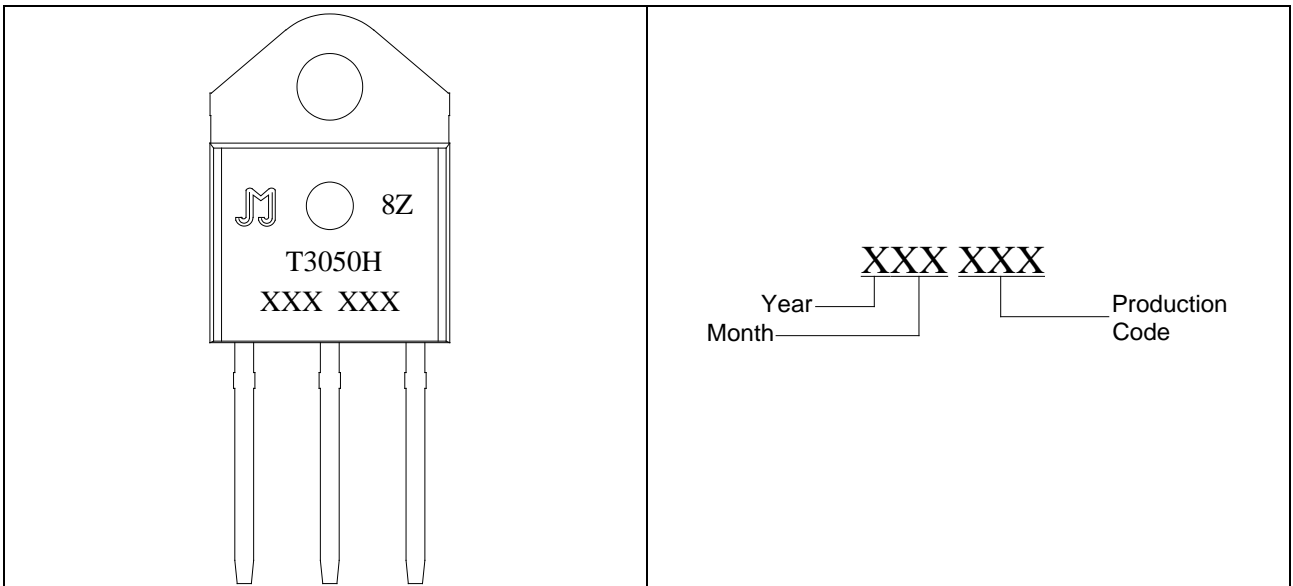
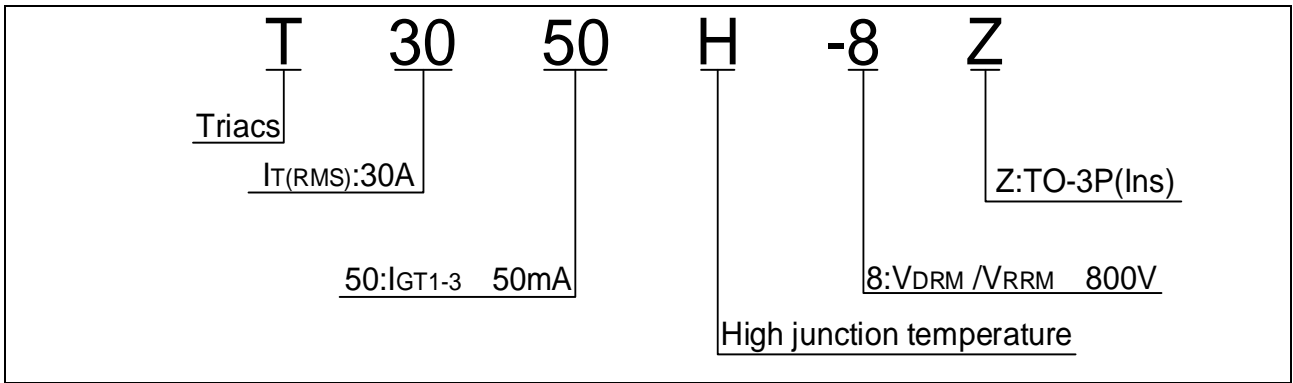


FIG 1: Maximum power dissipation versus RMS on-state current

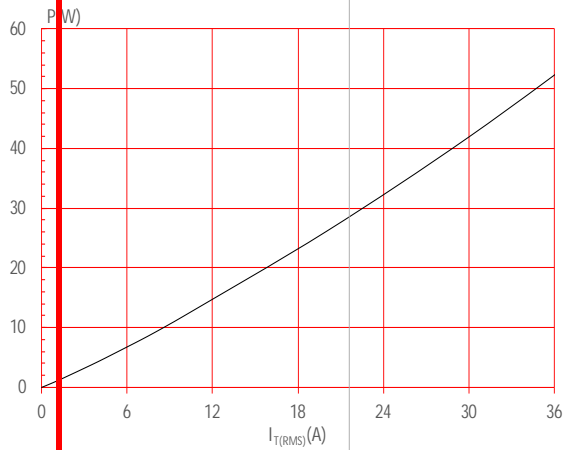


FIG.2: RMS on-state current versus case temperature

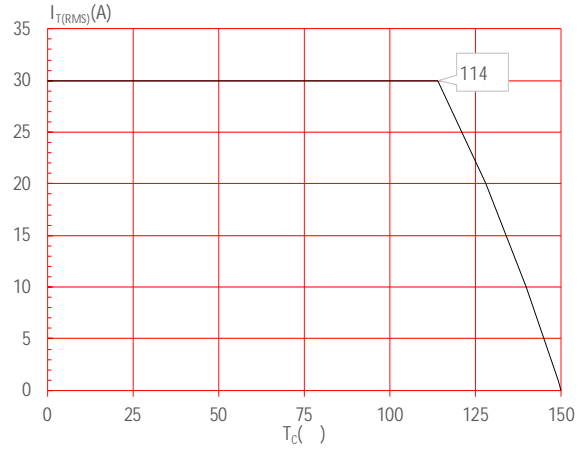


FIG 3: Surge peak on-state current versus number of cycles

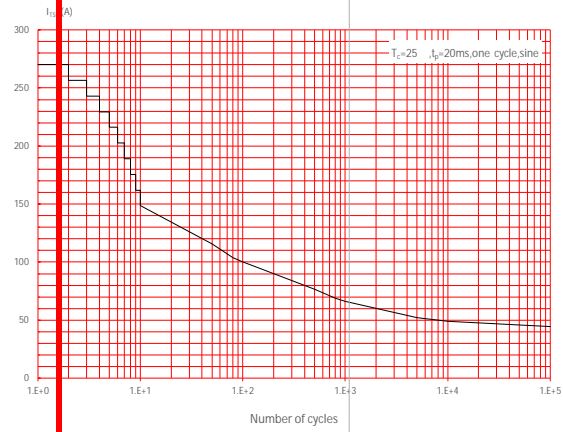


FIG.4: On-state characteristics

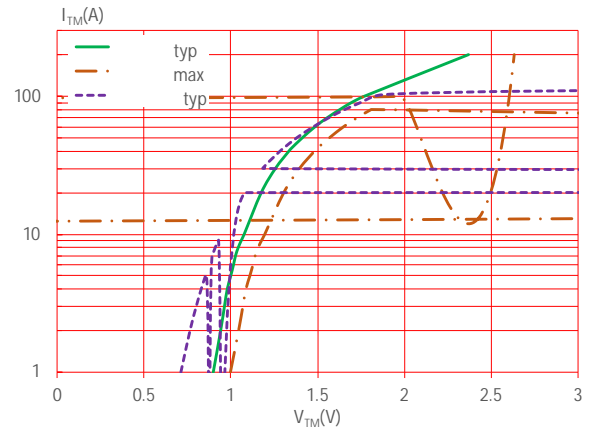


FIG 5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width t_p

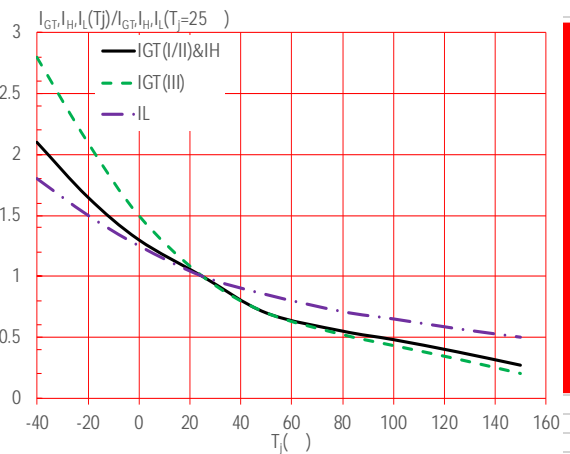
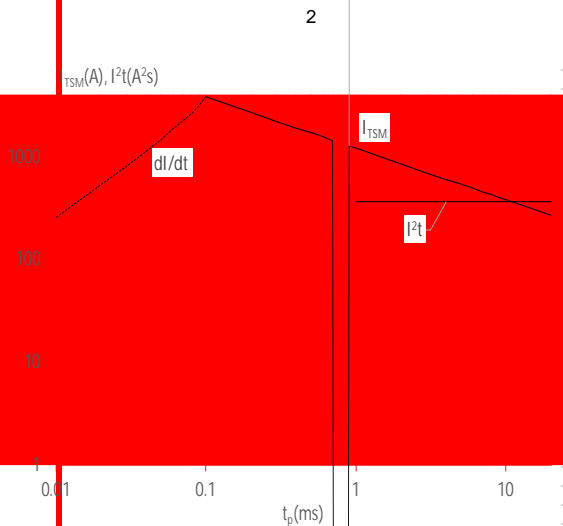
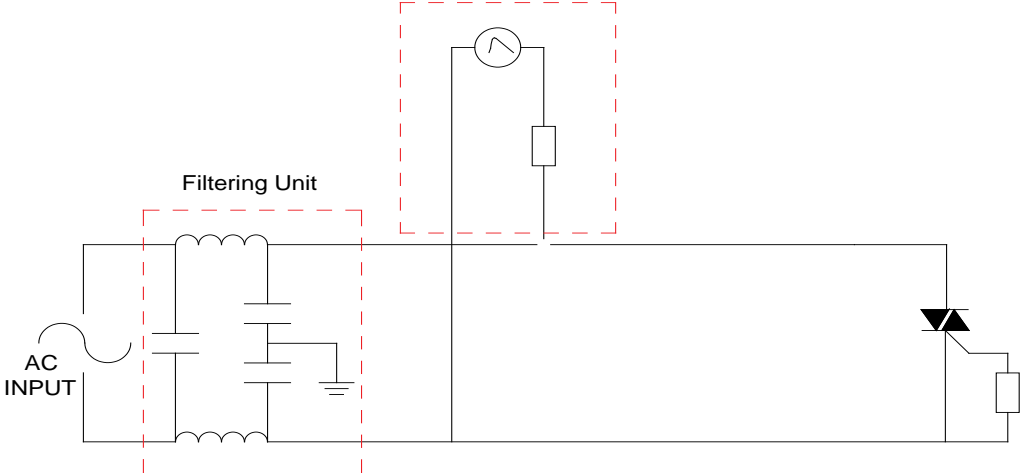


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

IEC61000-4-5 Standards
Surge Generator



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H I- J			
T3050H-8Z	800	50	TO-3P(Ins)	30	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.16, 2025		



Information furnished in this document is believed to be accurate and reliable. However,