



ORDERING INFORMATION

FIG.1: Maximum power dissipation versus RMS on-state current

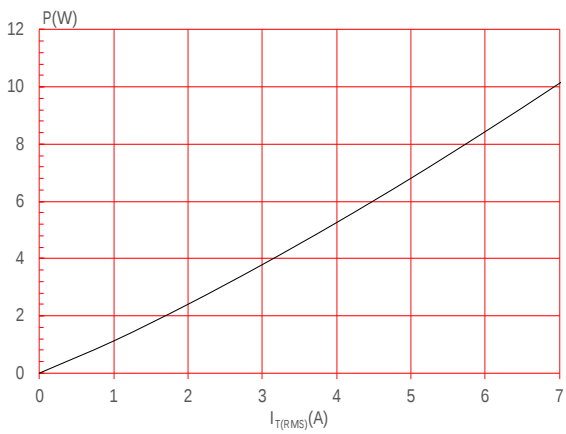
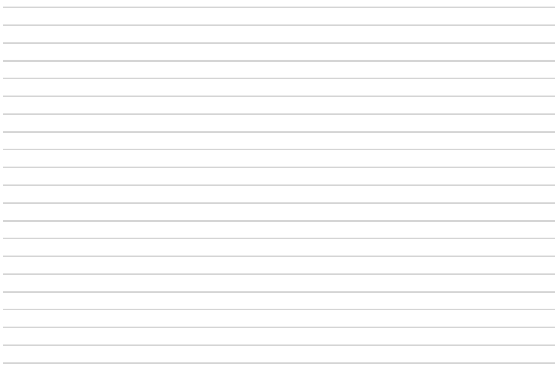


FIG.2: RMS on-state current versus case temperature



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Order code		Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	
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