

ORDERING INFORMATION

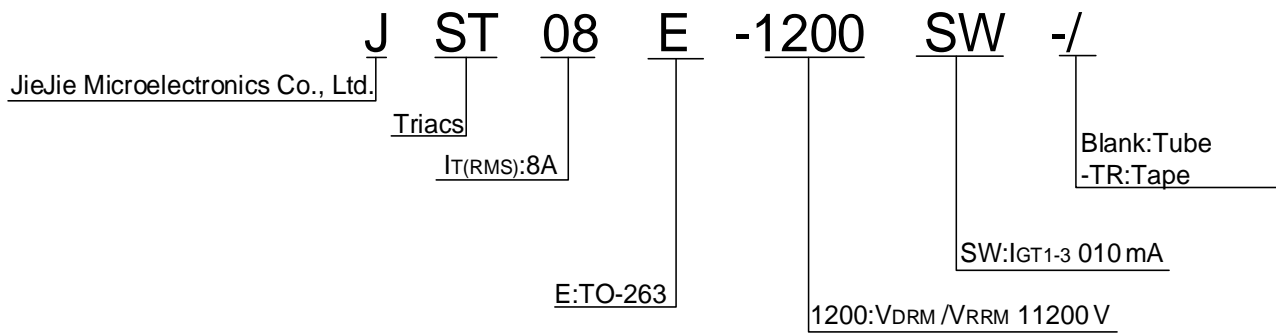


FIG.1: Maximum power dissipation versus RMS on-state current

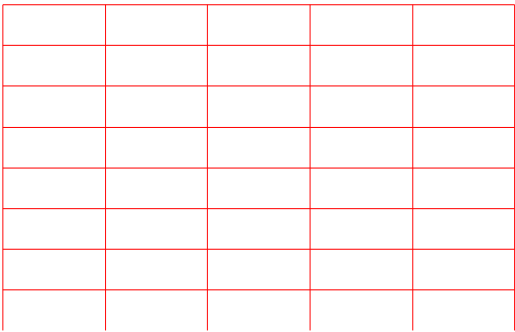


FIG.2: RMS on-state current versus case temperature θ

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



FIG.8 ÖTt circuit for inductive and rjoitive loado to IEC -61000-4-5 otandards

IEC61000-4-5 Standards
Surge Generator
6 Y R O W D J H V X U J H
6 F X U U H Q W V X U J H



