

25

tjuju rd= Σ



ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

I _{GT}	V _D =12V R _L =33Ω	- -	MAX.	5	mA
				10	
V _{GT}		ALL	MAX.	1.3	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3kΩ	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	- -	MAX.	10	mA
				20	
I _H	I _T =50mA		MAX.	7	mA
dV/dt	V _D =540V Gate Open T _j =110		MIN.	50	V/μs
(dV/dt) _c	(dI/dt) _c =0.3A/ms, T _j =110		MIN.	5	V/μs
t _{on}	I _G =20mA I _A =200mA I _R =20mA T _j =25		TYP.	2.5	μs
t _{off}				25	

STATIC CHARACTERISTICS

V _{TM}	I _{TM} =1A t _p =380μs	T _j =25	1.5	V
V _{TO}	Threshold voltage	T _j =125	1.01	V
R _D	Dynamic resistance	T _j =125	316	mΩ
I _{DRM}	V _D =V _{DRM} V _R =V _{RDM}	T _j =25	5	μA
I _{RDM}		T _j =125	0.25	mA

THERMAL RESISTANCES

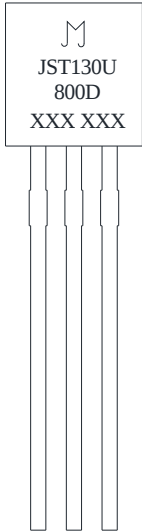
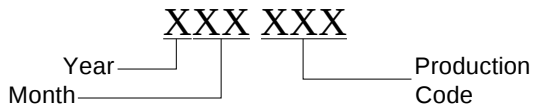
R _{th(j-c)}	junction to case (AC)	65	/W
R _{th(j-a)}	junction to ambient (AC)	150	/W



ORDERING INFORMATION

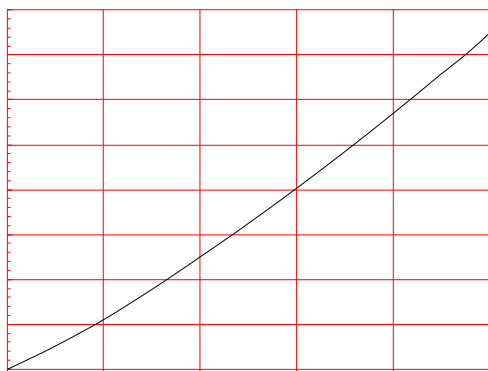
<u>J</u>	<u>ST</u>	<u>130</u>	<u>U</u>	<u>-800</u>	<u>D</u>	<u>-/</u>
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:0.8A$	$U:TO-92$	$800:V_{DRM}/V_{RRM} \quad 800V$	D:IGT1-3 5mA IGT4 10mA	Blank: Bulk Pack -TR: Tape & Reel

MARKING

	
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Maximum power dissipation versus RMS
on-state current



RMS on-state current versus
case temperature

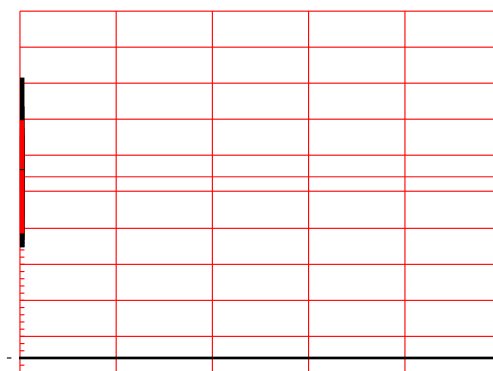
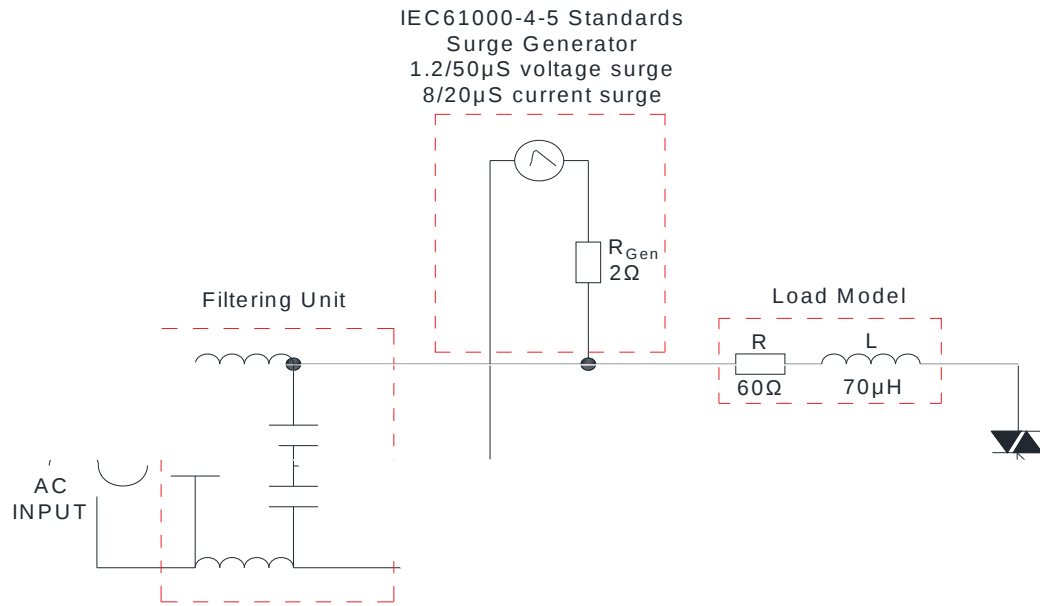


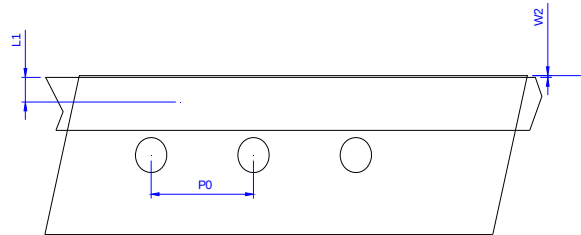
FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





DELIVERY MODE

TO-92	Bulk Pack	1,000	10,000	50,000



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
P	12.40	12.70	13.00	0.488	0.500	0.512
P0	12.40	12.70	13.00	0.488	0.500	0.512
P1	3.55	3.85	4.15	0.140	0.152	0.163
P2	5.95	6.35	6.75	0.233	0.250	0.265
P	-1.00	0	1.00	-0.039	0	0.039
F1 F2	2.30	2.50	2.70	0.090	0.098	0.106
F1-F2	-0.10	0	0.10	-0.004	0	0.004
W	17.50	18.00	19.00	0.689	0.709	0.748
W0	5.50	6.00	6.50	0.217	0.236	0.256
W1	8.50	9.00	9.50	0.335	0.354	0.374
W2			1.00			0.039
D0	3.80	4.00	4.20	0.150	0.157	0.165
H	-1.00	0	1.00	-0.039	0	0.039
L1	2.50			0.098		
H	18.00	19.00	20.00	0.709	0.748	0.787
H1 H2			3.00			0.119



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