TEL +86-513-68528666

Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	1	kV

(T$_j$=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	- -	MAX.	50	mA
				70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	- -	MAX.	70	mA
				100	
I_H	I_T=500mA		MAX.	60	mA
dV/dt	V_D=540V Gate Open T_j=125		MIN.	1000	V/μs
(dV/dt)c	(dI/dt)c=7A/ms, T_j=125		MIN.	12	V/μs
t_{on}	I_G=80mA I_A=400mA I_R=40mA T_j=25		TYP.	5	μs
t_{off}				50	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM}=22.5A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.77	V
R_D	Dynamic resistance	T_j=125	30	mΩ
I_{DRM}	V_D=V_{DRM} V_R=V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	0.5	mA

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W

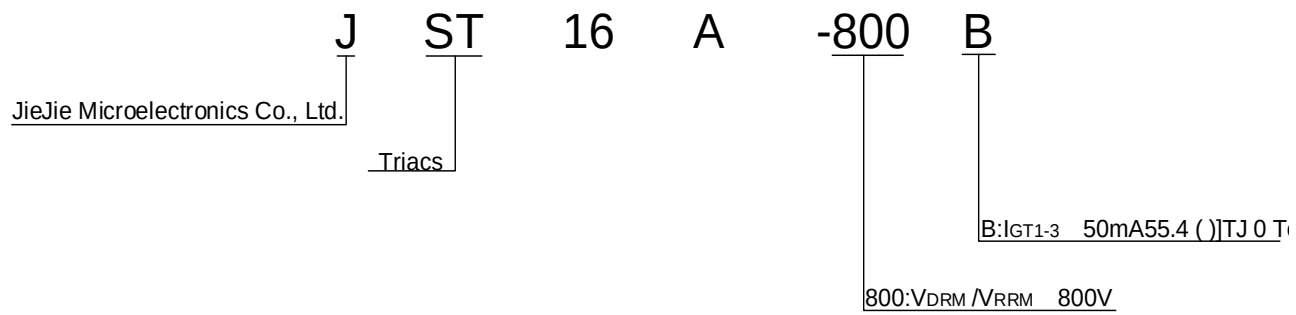

J ST 16 A -800 B
JieJie Microelectronics Co., Ltd.
Triacs
B:IGT1-3 50mA55.4 ( )]TJ 0 T
800:VDRM /VRRM 800V

FIG.1: Maximum power dissipation versus RMS on-state current

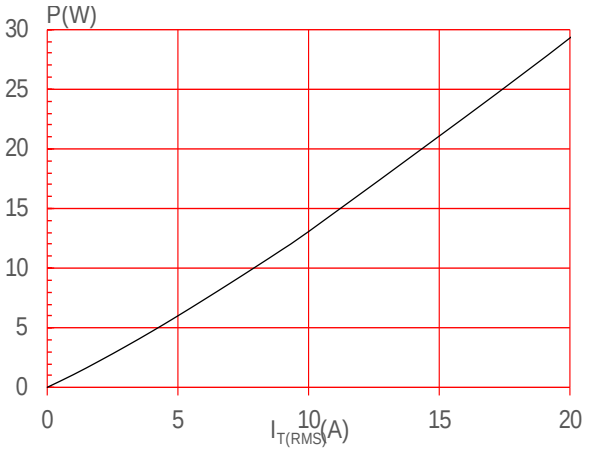


FIG.2: RMS on-state current versus case temperature

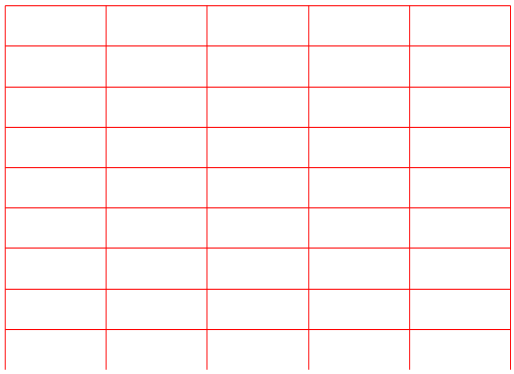
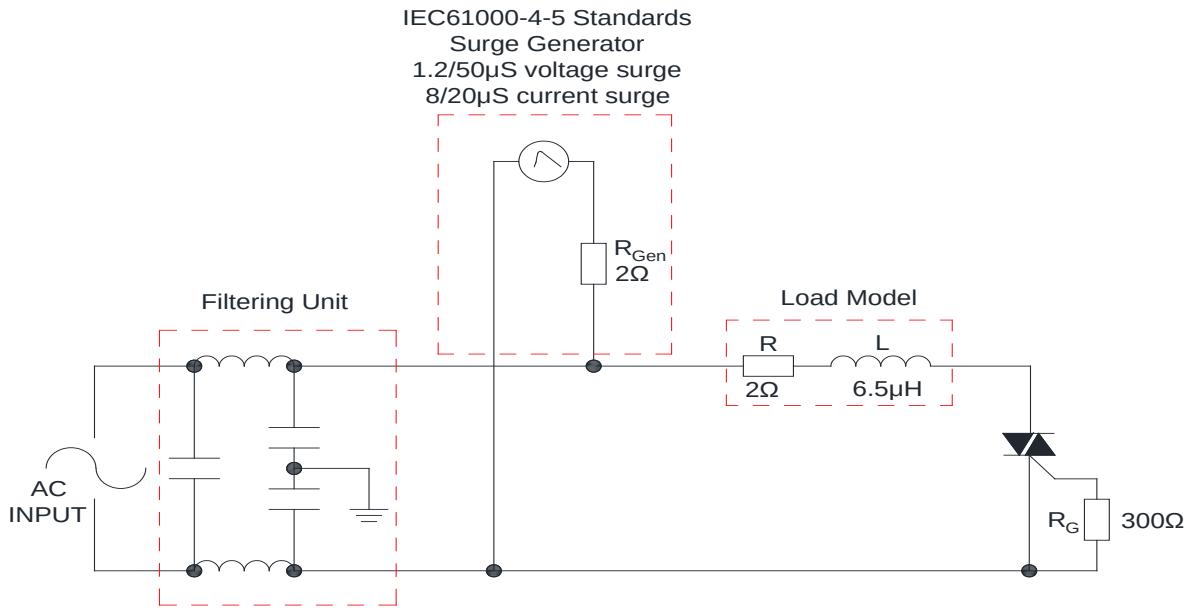
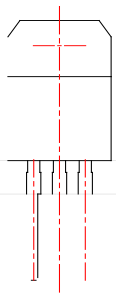


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Refer to the application note Assembly Instructions for Thyristors in Through-hole Package released by JieJie Microelectronics



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright ' 2026 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.