



DE



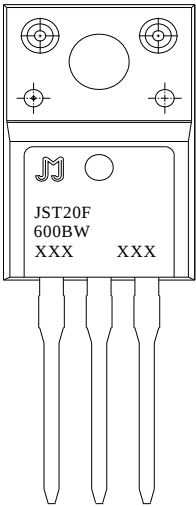
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25^{\circ}\text{C}$)			



ORDERING INFORMATION

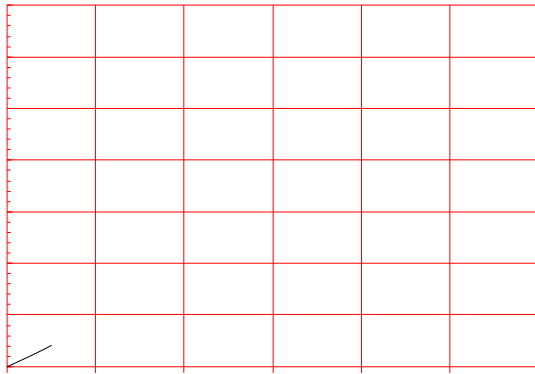
<u>J</u>	<u>ST</u>	<u>20</u>	<u>F</u>	<u>-600</u>	<u>BW</u>
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}: 20A$			
		$F: TO-220F(Ins)$			$BW: I_{GT1-3} \leq 50mA$
				$600: V_{DRM} / V_{RRM} \geq 600V$	

MARKING

	<u>XXX</u> <u>XXX</u> Year ——— Month ——— Production Code
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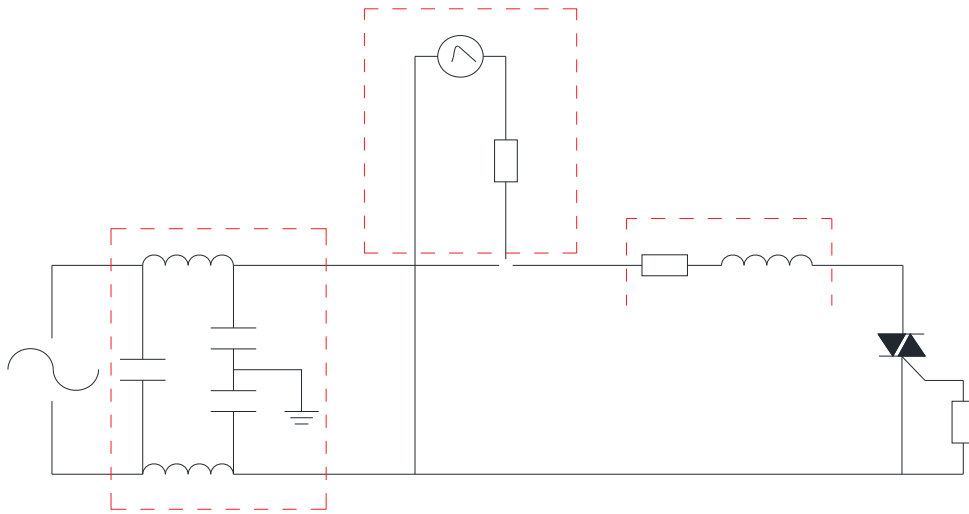


Maximum power dissipation versus RMS
on-state current



RMS on-state current versus case
temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





ORDERING INFORMATION

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.13, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

